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Assignment #2

CS 530

Software Design Document

**System Specification:**

**System Inputs:**

*<filename>.obj* File contains the Header record, Text records, Modification

records, and End records

*<filename>.sym* File contains the SYMTAB and LITTAB that references labels

by addresses

**System Outputs:**

*<filename>.sic* Source File

**Level of Error of Processing Required: //**TO DO

**Performance Requirements://**TO DO

**Design:**

Code will be written in C++.

There will be multiple files to handle the processing of the object program:

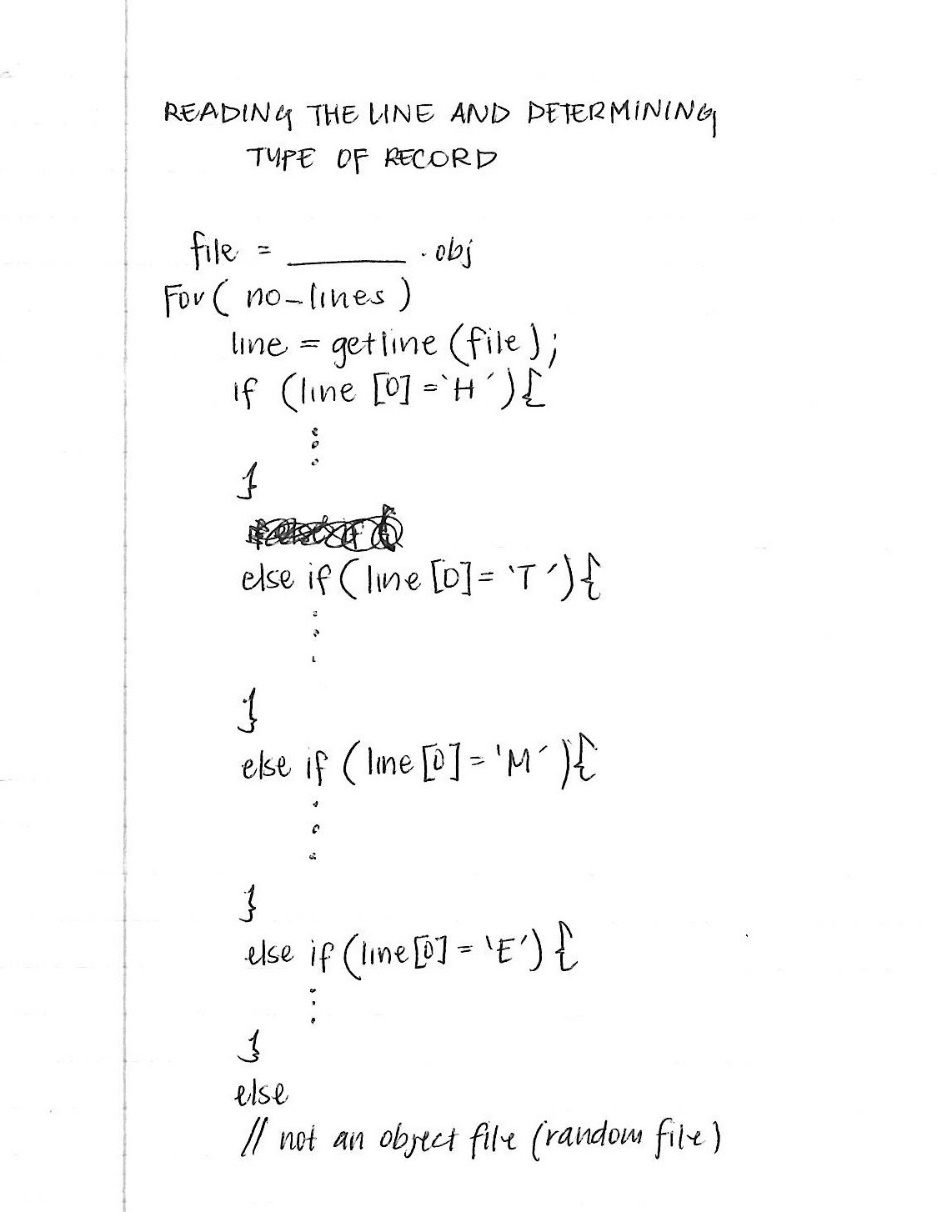
* Op code file (retrieval of machine code)
* Symbol table file (retrieval of labels from SYMTAB or LITTAB)
* Main file – combines all files to process object program

**System Software Design:**

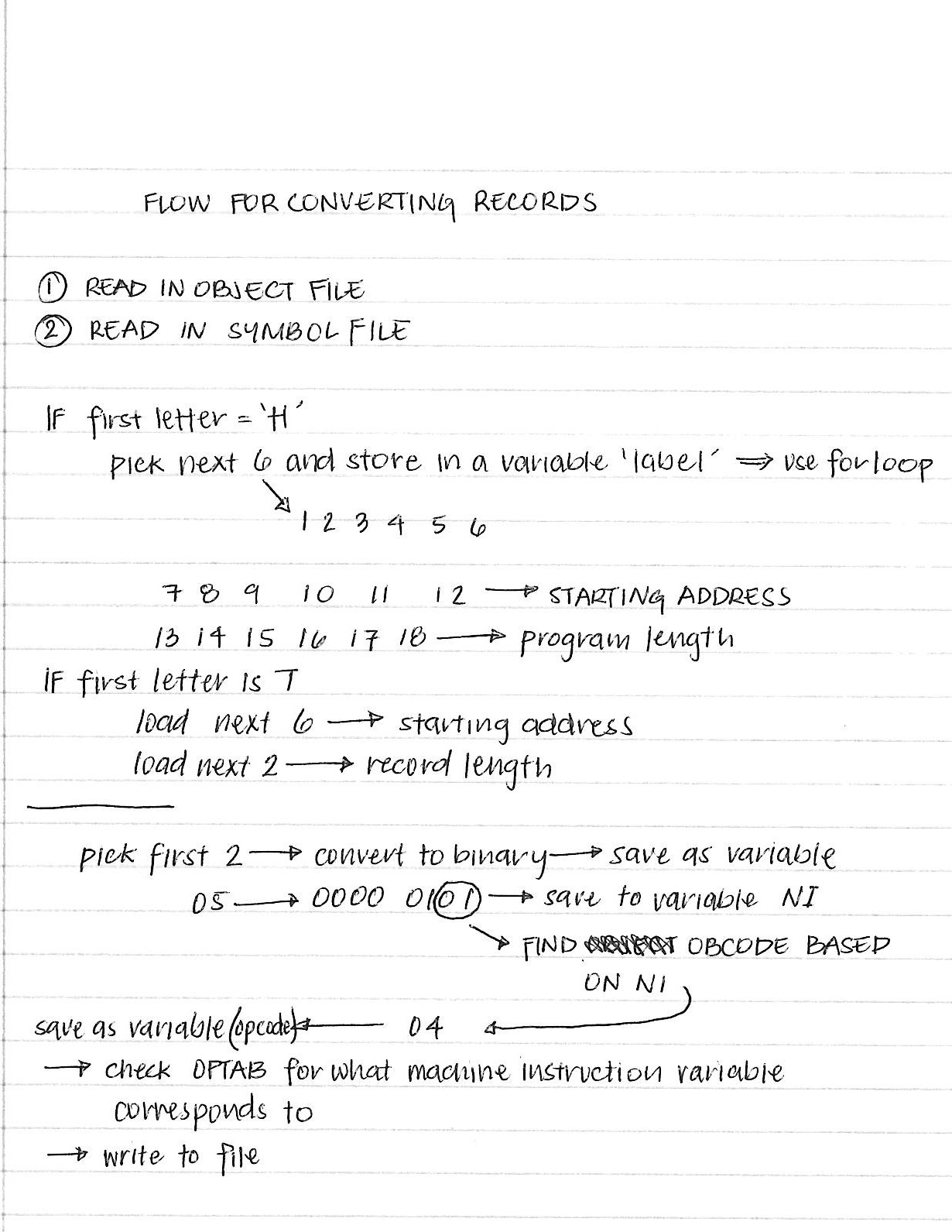
This assignment required a great deal of planning to create a SIC/XE disassembler. As a team, we had meetings, almost weekly, to determine the many elements that are necessary to make our disassembler work properly. Our meetings included topics such as determining the handling of different formats, how to calculate opcode, nixbpe bits, the extraction and retrieval of information from the SYMTAB/LITTAB and OPTAB, specific instructions that would need special treatment, the inclusion of certain variables to help our code work (program counter and location counter), assembler directives, and distinguishing an object program file from a random text file. Included in this document are diagrams, we as a team, developed during our meetings that will show how our thinking process. Note, that these diagrams are from our brainstorming sessions and that not all ideas were correct at the time. However, the ideas continually changed to be correct in the meetings after that. Also note, the diagrams were rewritten for better clarity.

In our first meeting as a group, we first strategized how to handle the .obj file that would be passed into our code. We also decided that we would be writing this disassembler in C++ as we thought some of the programming language’s libraries would benefit our code.

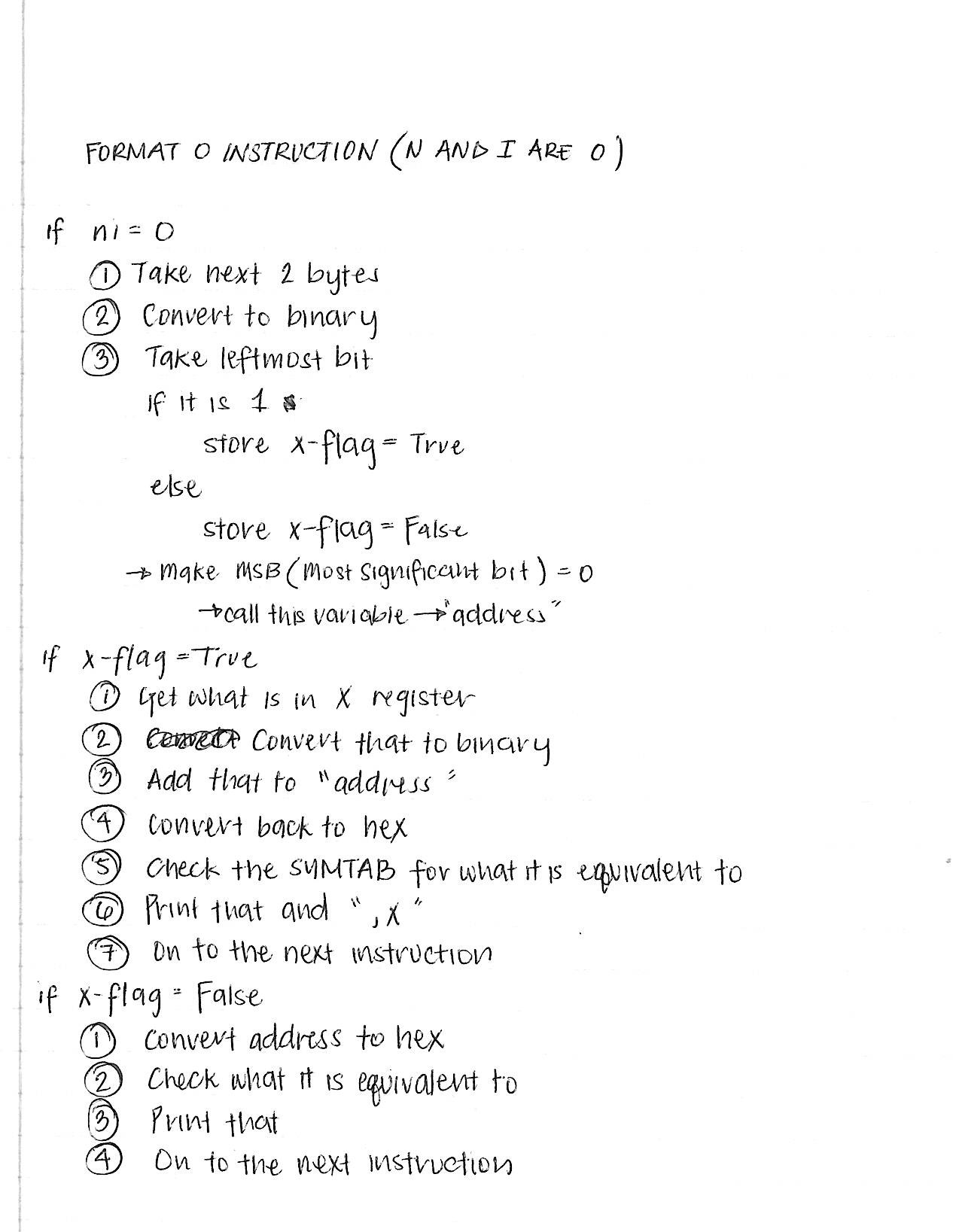
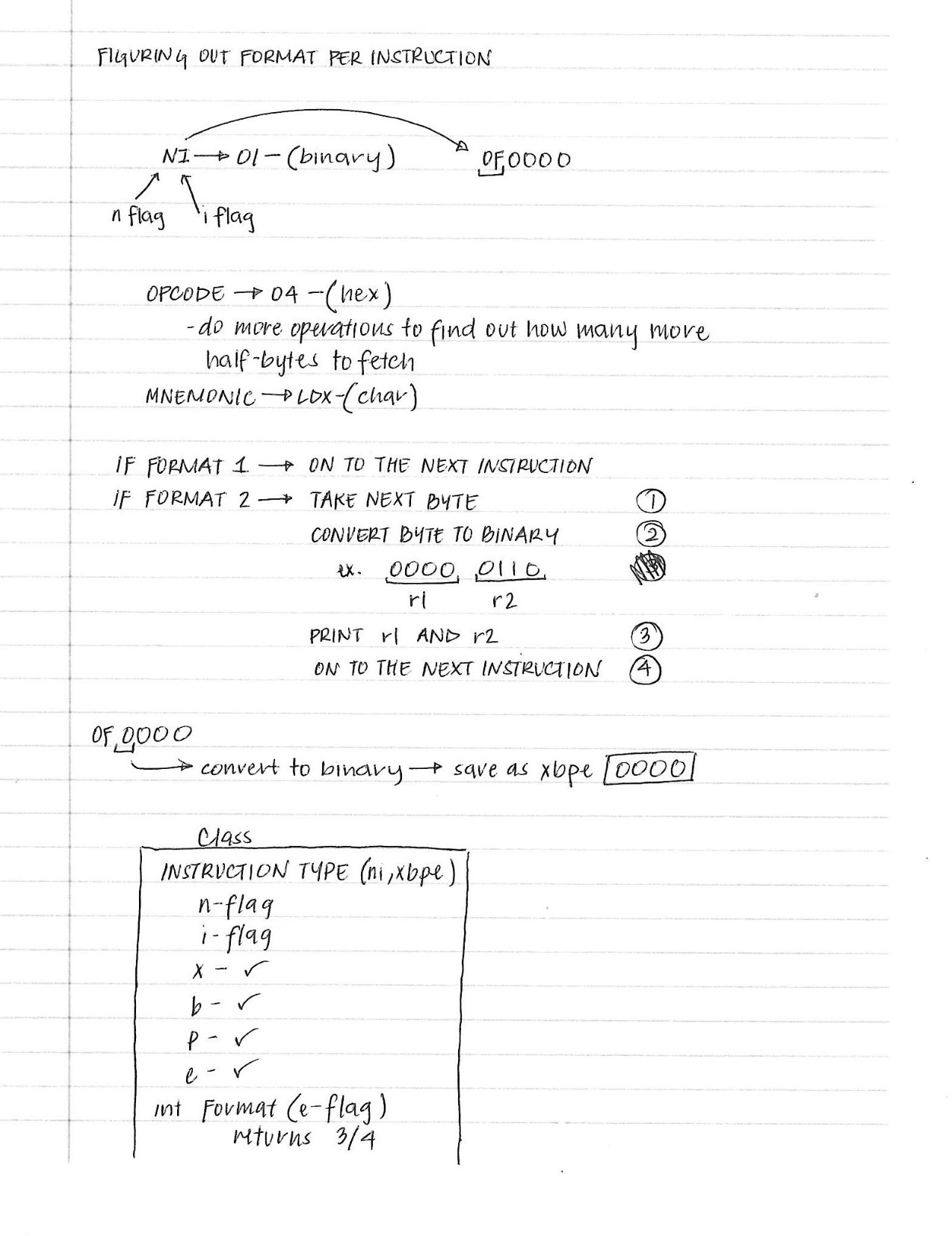
To determine that the .obj file is not just a random file with random letters and numbers, we would check the first character of each line. To be a valid object program file, the first character of the first line would be ‘H’ for Header record, the following lines’ first character would be ‘T’ for Text record, ‘M’ would be an acceptable first character in the lines after the Text records as it would imply it was a Modification record, and the last line’s first character would have to be an ‘E’ to signify the End record. If the first characters of each line do not follow this convention, then the code would stop from there.



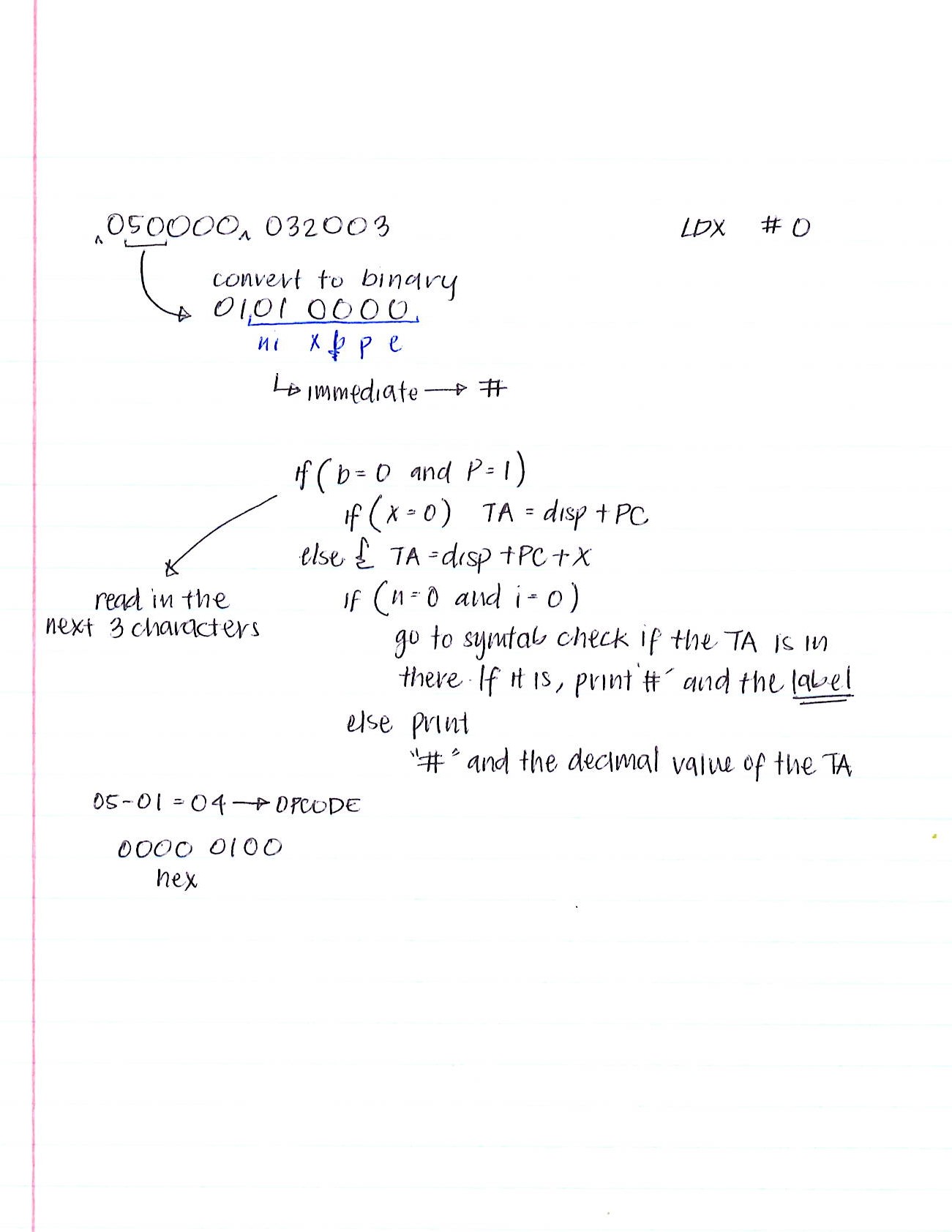
After determining that the file was an object program, the symbol table (.sym file) is read also.

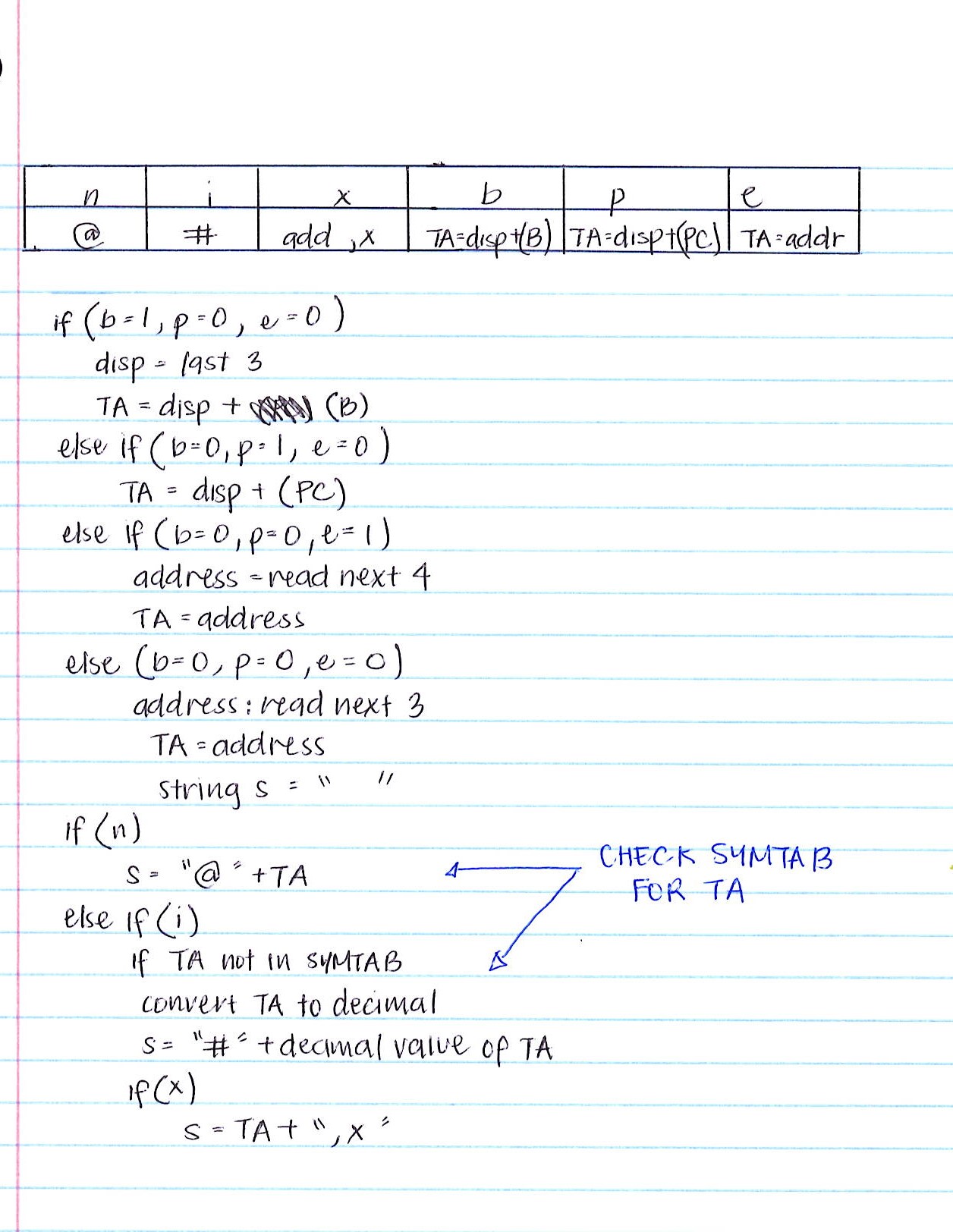
In the following diagram, we demonstrate, simply, how to work with the Header record and Text records of the object program. 

On the next meeting, we processed the handling of the different formatting (0, 1, 2, 3, 4) that one would encounter in SIC/XE disassembler. We all agreed that format 3 and 4 would be the trickier component we would have to focus on as we had to determine the handling of nixbpe bits.

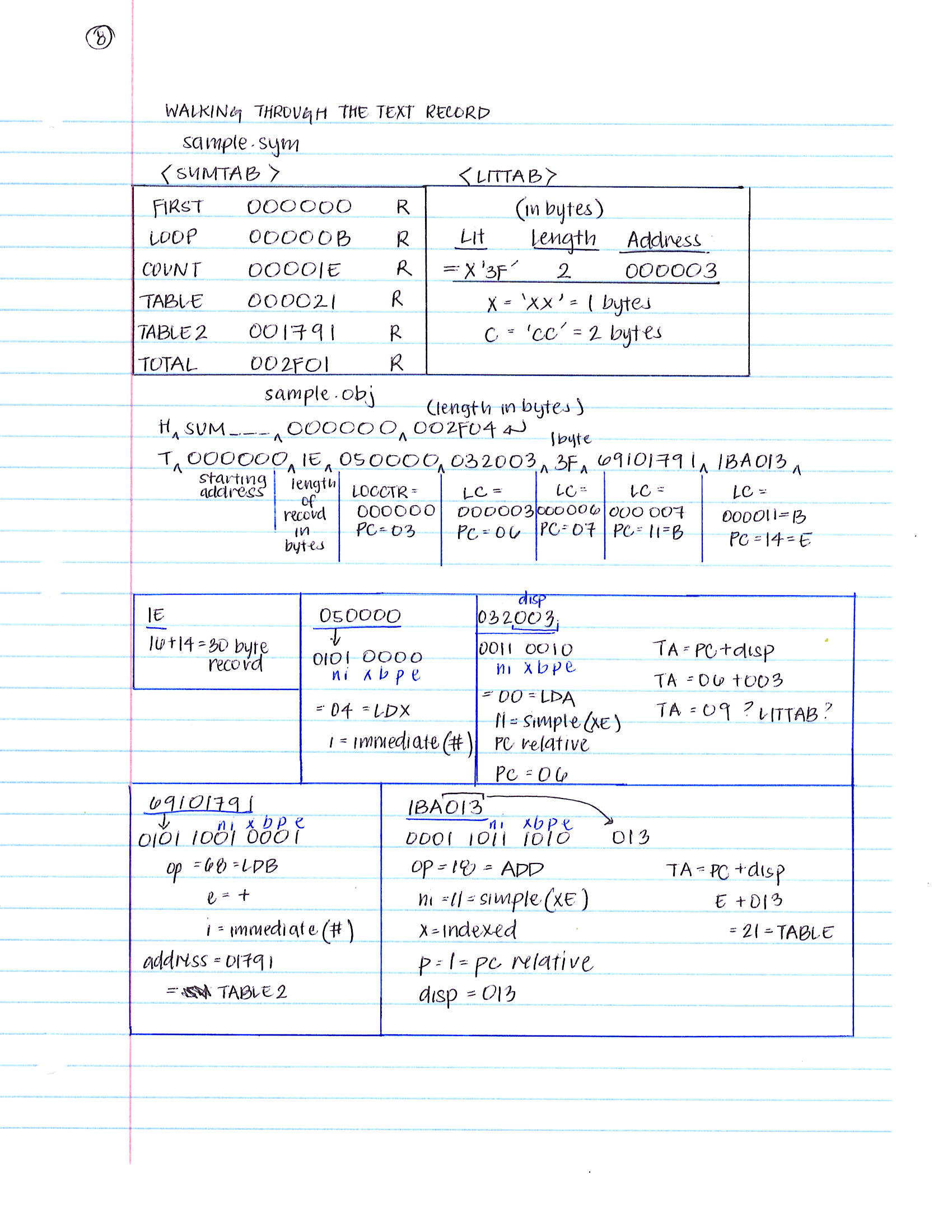
One of the ideas we considered including in our program was the use of classes. This class would handle the nixbpe bits of each opcode in the object program and determine the format of the opcode. In the end, we decided not to do classes for our program as we concluded it was necessarily needed. 

The following meetings included an in-depth look on how opcodes of format 3 and 4 instruction would be handled. We knew that the nixbpe bits would be significant in how our program would write the source file and focused on the different combinations of the bits that could happen. We worked through example object code to determine the many possibilities that occur that would how the source file’s contents.





After determining how to handle nixbpe bits of Format 3 and 4 instructions, we focused on the calculation of the target address of each opcode. We spent one long meeting discussing how to calculate the address of a certain label as we were getting confused on if we had to add or subtract the program counter from the display to get the target address. When we finally understood how to calculate the target address needed to find a label, we concentrated on how to process the information of each label in the .sym file, assembler directives, and the creation of an optab for operands. This work was done individually and is also the time that we started to finally code. We had another meeting to talk about problems we encountered while coding and the addition of certain variables or components that we thought were missing from our program.



The last week before we had to turn in the assignment, we worked on perfecting our coding tasks. We worked on completing our code so that they returned the correct information. We consistently checked for errors in our code by testing our code with the object file and symbol table file given to us. We checked with each other to clarify any mistakes in the code and misunderstandings of what our tasks would be.